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### IN THE UNITED STATES PATENT & TRADEMARK OFFICE

Inventor(s): KATIC, Ognjen; REY, Claudio Gustavo

Title: EQUALIZER AND EQUALIZATION METHOD FOR

RETURN-TO-ZERO SIGNALS

Date: 10 July 2003

Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

## Disclosure Statement Pursuant to 37 C.F.R. §1.56

Preliminary to the examination of this application, the applicant wishes to draw the Examiner's attention to the references listed on the attached copy of form PTO-1449. For the Examiner's convenience, copies of each of the listed references are submitted herewith.

#### **REMARKS**

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that each or all of the listed documents are material or constitute "prior art". If the Examiner applies any of the documents as prior art against any claim in the application and applicant determines that the cited documents do not constitute "prior art" under United States law, applicant reserves the right to present the relevant facts and law regarding the appropriate status of such documents.

Applicant further reserves the right to take appropriate action to establish the patentability of the disclosed invention over the listed documents, should one or more of the documents be applied against the claims of the present application.

Respectfully submitted,

By:

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## LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT [Form PTO-1449 (Modified)]

# Other Art

Examiner	ID	Author, Title, Date, Pertinent Pages, etc.
	OA: 1	Proakis, J.G. Digital Communications, 3rd Edition, McGraw-Hill, 1995.
	OA: 2	Xiong, F. Digital Modulation Techniques, Artech House, 2000.
	OA: 3	ITU-T Recommendation G. 775 - "General Aspects of Digital Transmission Systems: Loss of Signal (LOS) and Alarm Indication Signal (AIS) Defect Detection and Clearance Criteria", 1994.
	OA: 4	ITU-T Recommendation G. 823 - "The Control of Jitter and Wander Within Digital Networks Which Are Based on the 2048 kbit/s Hierarchy", March 1993.
	OA: 5	Telecordia (Bellcore) GR-499-CORE - "Transport Systems Generic Requirements (TSGR): Common Requirements", December 1995.

Examiner:	<del></del>	 
Date Considered:		 · · · · · · · · · · · · · · · · · · ·

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.